

Abstract

The arrangement and the respective display unit comprise a
5 microprocessor, a demagnetization circuit and a switched
mode power supply operating in a normal mode and in a low
power mode. The microprocessor is coupled via the same
output to the switched mode power supply for controlling the
low power mode and to the demagnetization circuit for
10 controlling a demagnetization operation. In a preferred
embodiment, the control signal from the microprocessor
provides a control for a low power burst mode, and is
further combined with a power on-indicative signal via a
logical AND combination for controlling the demagnetization
15 circuit.

Fig. 1